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Ultra-X Inc.-- Technical Glossary

... PCI transmits **32 bits** at a time in a 124-pin ... are for power supply and grounding) and **64 bits** in a ... PCs, the ninth chip is often used for **parity error checking**. ...

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LSI Logic Corporation - Support - The information, resources and ...

... PCI transmits **32 bits** at a time in a 124 ... across disks with some disks storing **error checking** and correcting ... and dedicates one drive to storing **parity** information ...

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SimpleTech Consumer Pages

... **Error checking** and correction functions are carried out on the ... uses larger groupings: 7 bits to protect **32 bits**, or 8 ... **Parity** would be the same car with warning ...

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Glossary of Terms

... bus width (16 bits compared to **64 bits**) than current ... On PCs, the ninth chip is often used for **parity error checking**. ... to the actual memory chips is **32 bits** wide ...

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Glossary

... the case of computers usually referring to an **error-checking** procedure in ... See also **parity**, **parity** bit. ... exchange data with the system's CPU either **32 bits** or **64** ...

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... deal with binary numbers of only **32 bits** or fewer ... its length (in bits) is just **64 bits** shy of ... H is the bit-wise XOR or "**parity (error checking)**" function; it ...

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... beat SDRAM data burst, or configured to be **32 bits** (36 bits ... Note: PAR[0:7] SDBA[1:0] **Error Checking** 1. Registered w/64-bit In-Line ECC 2. **Parity** Internal Bus ...

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... USER'S MANUAL. Cable set for I/O **Device**. ... Because the 72 pins SIMM module is **32 bits** width, using 2 PCs which can match a **64 bits** system. ...

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